



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor's Patent Application of:

INVENTORS: M. Voutilainen

SERIAL NO.: 10/684,169

ART UNIT: N/A

FILED: October 10, 2003

EXAMINER: Not yet assigned

TITLE: Method and Apparatus Employing PAM-5 Coding with Clock Embedded in Data Stream and Having a Transition when Data Bits Remain Unchanged

ATTORNEY DOCKET NO.: 872.0159.U1(US)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

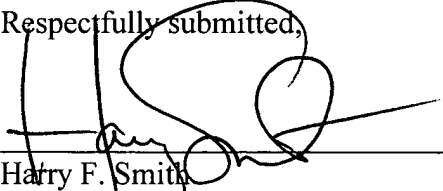
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,



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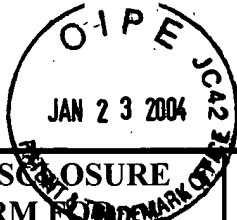


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

1/21/04
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Ann Okrentowich
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**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

Docket No.: 872.0159.U1(US)

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Applicant(s): M. Voutilainen

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Group: N/A

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
	US-				
	US-				
	US-				
	US-				
	US-				
	US-				
	US-				
	US-				

FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
	- -			
	- -			
	- -			
	- -			
	- -			
	- -			

OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

	KALIMAN et al., "Ternary Physical Protocol for MARILAN, a Multiple-Access Ring Local Area Network", IEEE 1988, 0195-623X/88/0000/0014, pgs. 14-20.
	SVENSSON et al., "A 3-Level Asynchronous Protocol for a Differential Two-Wire Communication Link", IEEE 1994, 0018-9200/94, pgs. 1129-1132.
	GUO et al., "Voltage Comparator Circuits for Multiple-Valued CMOS Logic", IEEE Computer Science 2002, Proceedings of the 32 nd Intl. Symposium on Multiple-Valued Logic (ISMVL'02), 0195-623X-02, 7 pages.
	HAN et al., "A Current-Mode Folding/Interpolating CMOS Analog to Quaternary Converter Using Binary to Quaternary Encoding Block", IEEE Computer Science 2002, Proceedings of the 32 nd IEEE Intl. Symposium on Multiple-Valued Logic (ISMVL'02), 0195-623X/02, 6 pgs.
	TABOREK, et al., "Multi-Level Analog Signaling Techniques for 10 Gigabit Ethernet", IEEE 802.3 Tutorial, 86 pgs.

Examiner's Signature:

Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
Include a copy of this citation form with your next correspondence to the Applicant(s).